

Docket No.: 43890-555

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :
Toyonori KANETAKA, et al. :
Serial No.: : Group Art Unit:
Filed: December 12, 2001 : Examiner:
For: METHOD OF MANUFACTURING CHIP INDUCTOR

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents and Trademarks
Washington, D. C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed with the application and no certification or fee is required.

A copy of the foreign search report is attached for the Examiner's information. Please note this is a PCT application in the entry of the National Phase in the U.S. However, to ensure that these references are available to the Examiner, we are providing copies of these references herewith. Since the Search Report was from the U.S. JPO or EPO search authorities, copies of these references should have been supplied to the USPTO under

Serial No.:

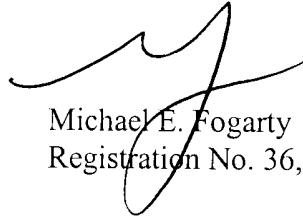
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531 Rec'd PCT/PTO 12 DEC 2001

the trilateral agreement and are believed to be in the file of the above identified application and readily available to the Examiner. Therefore it is believed that Applicants have met all requirements regarding duty of disclosure under 37 CFR 1.56. Acknowledgement and consideration of these documents are respectfully requested.

Respectfully submitted,

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INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION

(PTO-1449)

ATTY. DOCKET NO.
43890-555

SERIAL NO.

10/009750

APPLICANT

Toyonori KANETAKA, et al.

FILING DATE

December 12, 2001

GROUP

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
	11-3820	1/6/1999	Japan (w/ English Abstract)				
	10-125533	5/15/1998	Japan (w/ English Abstract)				
	2000-64094	2/29/2000	Japan (w/ English Abstract)				
	2000-58336	2/25/2000	Japan (w/ English Abstract)				
	2000-30952	1/28/2000	Japan (w/ English Abstract)				
	62-286211	12/12/1987	Japan (w/ English Abstract)				
	11-260647	9/24/1999	Japan (w/ English Abstract)				
	8-78290	3/22/1996	Japan (w/ English Abstract)				
	6-96952	4/8/1994	Japan (w/ English Abstract)				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.